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Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E

PATENT DATE

SCANNED

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APPLICATION NO. 09/893577	CONT/PRIOR F	CLASS 438	SUBCLASS 719	ART UNIT 1765	EXAMINER — Chen, K
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APPLICANTS

Masahiro	Tadokoro
Masahiro	Shioya
Masayuki	Kojima
Takenobu	Ikeda

Fabrication method of semiconductor integrated circuit device

TITLE

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) (Date)		ISSUE FEE	
Amount Due \$ Date Paid \$				
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____			ISSUE BATCH NUMBER	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			

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